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(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date	
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).	of
 DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. 	ıe
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 5. ☐ Notice of Informal Patent Application (PTO-152)	ì
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 6. ☐ Interview Summary (PTO-413),	
Paper No./Mail Date	
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9. Other	
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U.S. Patent and Trademark Office PTOL-37 (Rev. 7-05)

EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

Allowable Subject Matter

- 1. Claims 46, 48 and 50 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

The closest prior art in USPN 5,225,273 to Mikoshiba et al. (hereinafter referred to as Mikoshiba). Mikoshiba discloses that it is known in the art of touch panels (column 1, lines 14-23) to form an ITO transparent conductive film by sputtering followed by heat aging performed at a temperature of between about 100 to about 250°C (column 9, line 32 through column 10, lines 64). Mikoshiba specifically teaches a heat aging temperature of 150°C in Examples 1-6 (see column 12, lines 43-44 and column 13, lines 55-56). Examples 1-4 of applicant's specification clearly disclose that a sputter deposited transparent conductive ITO film heat aged at a temperature of about 150°C produces a film possessing a mean crystal size within the range of 40 to 100 nm, an arithmetic mean roughness (Ra) of 0.4 nm \leq Ra \leq 2.3 nm, and a root-meansquare roughness (Rms) of 0.8 to 0.9 nm. In comparison, Comparative Examples 1-2 of applicant's specification clearly disclose that a transparent conductive ITO film that is sputter deposited in the same way as in Examples 1-4, except that the heat aging process is omitted, possesses a mean crystal size within the range of 10 to 20 nm, an arithmetic mean roughness (Ra) of 0.1 nm \leq Ra \leq 0.25 nm, and a root-mean-square roughness (Rms) of 0.55 nm. Clearly, the heat aging step is directly related to the arithmetic mean roughness and the root-mean-square roughness.

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Considering that the ITO transparent conductive film taught by Mikoshiba is formed by a substantially identical method (sputtering followed by heat aging at about 150°C) compared to the method disclosed by the current applicant in Examples 1-4, it appears that the transparent conductive film of Mikoshiba possesses the claimed arithmetic mean roughness and root-mean-square roughness. Mikoshiba also discloses that the transparent conductive electrode may comprise crystal grain aggregates (see the paragraph bridging columns 3 and 4). Considering that the applicant discloses that the presence of crystal grain aggregates is responsible for the currently claimed arithmetic mean roughness and root-mean-square roughness (see page 19, line 23 through page 20, line 7 of applicant's specification), it appears that the transparent conductive film of Mikoshiba possesses the claimed arithmetic mean roughness and root-mean-square roughness.

Although Mikoshiba appears to teach to the claimed roughness values, Mikoshiba does not teach or suggest forming the film by a coating or printing process using a sol-gel material. Mikoshiba fails to teach or suggest how to form the claimed film, with the claimed roughness values, with a sol-gel material.

3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andrew T. Piziali whose telephone number is (571) 272-1541. The examiner can normally be reached on Monday-Friday (8:00-4:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Terrel Morris can be reached on (571) 272-1478. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

atp

ANDREW T. PIZIALI
PATENT EXAMINER

TERREL MORRIS
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 1700

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